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REMARKS

The Office Action of 02/20/2007 has been carefully considered. Reconsideration in view of the foregoing amendments and the present remarks is respectfully requested.

Claims 1-3, 9 and 10 were rejected as being anticipated by Tsai. This rejection is respectfully traversed.

As made clear in claims 1 and 9, the present invention involves programming a non-volatile memory by first transferring at least part of the data to be written to the non-volatile memory to *volatile* memory. A wait signal is sent to the processor. The data is transferred from *volatile* memory to non-volatile memory. Then the wait signal is removed. Tsai does not teach or suggest any such combination of features.

Rather, Tsai teaches transferring data to a loader block of *non-volatile* memory and subsequently transferring the data to a user block of non-volatile memory. Accordingly, it may be seen that Tsai fails to anticipate the present invention.

Withdrawal of the rejections and allowance of claims 1-12 is respectfully requested.

Respectfully submitted,



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